

## **Environmental specifications**

			Specification	Note	
	Enclosure	Minimum	5°C		
Operating ambient	space	Maximum	40°C	Permanent installation	
temperature	Open	Minimum	5°C	remanent installation	
	space	Maximum	55°C		
S	torage temperature		-25~70°C		
Relative hur	midity(non-condensing	g, RH-2)	5~95%		
F	Pollution resistance		Degree II		
C	orrosion resistance		Base on IEC-68 standard		
	Altitude		≤2000m		
Vibration	Fixed by DIN	RAIL	0.5G, 2 hours for each direction of 3 axes		
resistance	Fasten by so	crew	2G, 2 hours for each direction of 3 axes		
	Shock resistance		10G, three times for each direction of 3 axes		
	Noise resistance		1500 Vp-p, pulse width 1μS		
	Withstand voltage		1500VAC, 1 minute	L, N to any terminal	

## **AC** power supply specifications

Specification Item		10/14 points main units	20/24 points main units	32/40 points main units	60 points main units		
Input range	Voltage	100~240VAC, -15%/+10%					
Input range	Frequency	50/60Hz ±5%					
Max. power consumption (bui	ilt-in power supply)	21W(SPW14-AC) 36W(SPW24-AC)					
Inrush curre	nt	20A@264VAC					
Allowable power momentary	interruption time	< 20mS					
Fuse rating		2A, 250V					

## DC power supply specifications

Specification Item	10/14 points main units	20/24 points main units	32/40 points main units	60 points main units	
Input voltage	12 or 24 VDC, -15%/+20%				
Max, power consumption (@ full built-in power supply)	21W(SPW14-D12/D24) 36W(SPW24-D12/D24)				
Inrush current	20A@12 or 24VDC				
Allowable power momentary interruption time	< 2mS				
Fuse rating	3A(D12)/1.5A(D24),125V	24),125V 5A(D12)/2.5A(D24),125V			

## **Main unit specifications**

\*: Default, changable by user

		Item	Specification	Note
	Execut	tion speed	0.33uS/Sequential instruction	
	Progra	m capacity	20K Words	
	Progra	m memory	FLASH ROM or SRAM + Lithium battery for Back-up	
	Sequenti	al instruction	36 instructions	
	Function	n instruction	326 instructions (126 kinds)	Include derivative instructions
Flo	w chart o	command (SFC)	4 instructions	
	Port 0 (RS232 or USB)		Communication speed 4.8k ~ 115.2Kbps (9.6Kbps)*	
Communication Interface	Port 1 ~ Port 4 (RS232, RS485 , Ethernet, CANopen or GSM)		RS232, RS485, Ethernet, CANopen Communication speed 4.8k ~ 921.6kbps (9.6kbps)*	
		Maximum link stations	254	
	Х	Input contact (DI)	X0~X255 (256)	Corresponding to external digital input
Digital (Bit status)	Υ	Output relay (DO)	Y0~Y255 (256)	Corresponding to external digital output
	TR Temporary relay		TR0~TR39 (40)	

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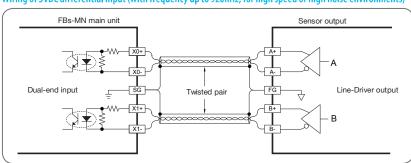
		lter	n			Speci	fication		Note	
				Non-retentive	M0 ~ M799 (800	)*			Can be configured as retentive type	
	M	Internal relay		Non-retentive	M1400 ~ M1911	(512)				
Die '	IVI			Retentive	M800 ~ M1399 (	600)*			Can be configured as non-retentive type	
gital		Special relay		'	M1912 ~ M2001	(90)				
Digital (Bit status)	S	Step relay		Non-retentive	\sqrt{0} \sim \sqrt{499} (500)*		S20 ~ S499 can be configured as retentive type			
				Retentive	S500 ~ S999 (50	0)*			Can be configured as non-retentive type	
	Т	Timer "Time-Up"	status c	ontact	T0 ~ T255 (256)					
	С	Counter "Count-U	p" statu	s contact	C0 ~ C255 (256)					
			0.018	Time base	T0 ~ T49 (50)*					
	TMR	Timer current	0.1S T	ime base	T50 ~ T199 (150)	*			T0 ~ T255 numbers for each time base ca	
		value register	1S Tim	ne base	T200 ~ T255 (56)	)*			be adjusted.	
			16-bit	Retentive	C0 ~ C139 (140)*				Can be configured as non-retentive type	
	CTR	Counter current	10 01	Non-retentive	C140 ~ C199 (60)	)*			Can be configured as retentive type	
	OIII	value register	32-bit	Retentive	C200 ~ C239 (40	)*			Can be configured as non-retentive type	
			JZ-bit	Non-retentive	C240 ~ C255 (16	)*			Can be configured as retentive type	
	LID			Retentive	R0 ~ R2999 (300	0)*			Can be configured as non-retentive type	
JD	HR DR			netentive	D0 ~ D3999 (400	00)				
egis	DIT			Non-retentive	R3000 ~ R3839 (	840)*			Can be configured as retentive type	
Register (Word data)	Ш	Data register		Retentive	R5000 ~ R8071 (	3072)*			When not configured as ROR, it can servinormal register (for read/write)	
ord dat	HR ROR	Read only register		R5000 ~ R8071 can be set as ROR ~ default setting is (0)*			ROR is stored in special ROR area and no occupy program space			
<u>B</u>				File register	F0 ~ F8191 (8192)				Save/retrieved via dedicated instruction	
	IR	Input register			R3840 ~ R3903 (	64)			Corresponding to external numeric inpu	
	0R	Output register		R3904 ~ R3967 (64)				Corresponding to external numeric outp		
		Special system register		R3968 ~ R4167 (197), D4000 ~ D4095 (96)						
		0.1mS high-speed timer register		R4152 ~ R4154 (3)						
	SR	High-speed Hardware (4 sets)		DR4096 ~ DR411	10 (4x4)					
		counter register Software (4 sets)		DR4112 ~ DR4126 (4x4)						
	-			R4128 (sec)	R4129 (min)	R4130 (hour)	R4131 (day)			
		Calendar Register		R4132 (month)	R4133 (year)	R4143 (week)	,,,	Optional for MA model		
	XR	Index register			V · Z (2), P0 ~ P9 (10)					
errup		External interrupt	control				sitive/negative ed	ne)		
arrol	l	Internal interrupt				, 3, 4, 5, 10, 50, 10		9-7		
	niah sne	ed timer(HST)	50111101		·					
1110 1	ligii apo	od tillior(HOT)		No. of channel	Up to 4	1 (16-bit), 4 (32-bit, share with HHSC)				
_	Howeling	بروم اموموم وامتوا مسر	- H	Counting mode	-1			-		
ligh:		are high-speed cou L/32-bit	counter Counting mode		8 modes (U/D, U/Dx2, P/R, P/Rx2, A/B, A/Bx2, A/Bx3, A/Bx4)				• Total number of HHSC and SHSC is 8	
High-speed	(111100)	ISC) /32-bit Counting frequency			Maximum is 200KHz (Single-end input) or 920KHz (differential input)			HHSC can be converted into 32-bit/0.1r time base High-Speed Timer (HST)		
d counter	Softwa	re high-speed cour		No, of channel	Up to 4				Half of maximum frequency while A/B input	
nter		re nigri-speed coui /32-bit	11101	Counting mode	3 modes (U/D, P.	/R, A/B)			mpat	
	(5.,55)		(	Counting frequency	Maximum sum u	up to 5KHz				
		Number of axis			Up to 4					
		Output frequency	у			_	output) or 920KH	Z	Half of the maximum while A/B output	
sition se ou		Pulse output mo	de		(differential output) 3 modes (U/D, P/R, A/B)					
SPSO		Programming me	ethod		Dedicated posit	ion language				
		Interpolation	011100		·	s linear interpola	tion			
		· ·	e		Up to 4	3 Inicai interpola				
PWN put	1	Number of points  Output frequency		72Hz ~ 18.432KHz (with 0.1% resolution)						
		,		ointo		(Hz (with 1% res		table this factory		
			P	oints			main unit are sui	able this feature)		
pture	d input		I.	linimum capturable		high speed / hig				
				ulse width		ium speed input				
					>470 μS (for Me	dium low speed	nput)			
			V	0 ~ X15	Adjustable frequ	uency 14KHz ~ 1.	8MHz		Chosen by frequency at high frequency	
gital f	ilter		1	U ~ AIJ	Adjustable time	constant 0 ~ 1.5	mS/0~15mS ( unit	0.1mS/1mS)	Chosen by time constant at low frequen	
			Y	16 ~ X35	Time constant 1	~ 15mS adjustal	le (unit: 1ms)			



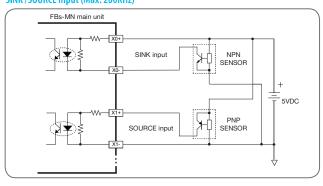
### Digital Input (DI) Specifications

	Item	5VDC differential input						
Specification		Ultra high speed	High speed	Medium speed(HSC)	Medium low speed (capture input)	Low speed	Notes	
Maximum input frequency*/ accumulated time		920KHz	200KHz	IKHz 20KHz(HHSC) 0.47mS 4.7 Total 5KHz(SHSC)		4.7mS		
Input sig	nal voltage	5VDC ± 10%		24VE	DC ± 10%			
Threshold	ON	>11mA	>8mA	>4mA >2.3r		>2.3mA	* 11-16 - 6	
current	OFF	<2m	A <1.5mA		<0.9mA	*: Half of maximum frequency while A/B		
Maximum	input current	20mA	10.5mA	7.6mA		4.5mA	phase input	
Input in	ndication		Displayed by LEI					
Isolatio	n method		Opti					
SINK/SOURCE wiring		Independent wiring	Via variation of internal common terminal S/S and external common wiring					
Noise filtering methods		DHF (0~1 +AHF (0.4		DHF (0~15mS) +AHF (4.7μS)	DHF (0~15mS) +AHF (0.47mS)	AHF (4.7mS)	DHF: Digital Hardware Filter AHF: Analog Hardware Filter	

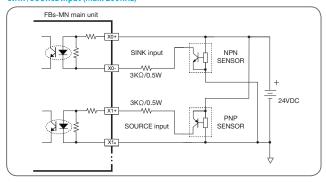
### Wiring of 5VDC differential input (with frequency up to 920KHz, for high speed or high noise environments)



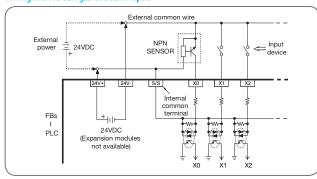
## Wiring of 5VDC differential input to 5VDC single-end SINK/SOURCE input (Max. 200KHz)



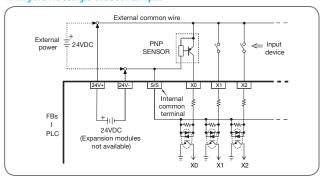
## Wiring of 5VDC differential input to 24VDC single-end SINK /SOURCE input (Max. 200KHz)



### Wiring of 24VDC single-end SINK input



### Wiring of 24VDC single-end SOURCE input

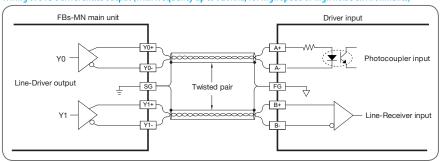


## Digital Output (DO) Specifications

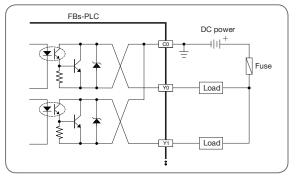
	Item	Differential output		Single-end				
Specification		Ultra high speed	High speed	Medium speed	Low speed	relay output		
Maximur	n output frequency*	920KHz	200KHz	20KHz	_	_		
Wo	orking voltage	5VDC±10%		5~30 VDC		< 250VAC/30VDC		
Maximum load	Resistive	50mA	0.5A	0.5A	0.5A/0.1A (24YT/J)	2A/single, 4A/common		
current	Inductive	John	0.5A	0.5A	0.5A/0.1A (241 1/5)	80VA(AC)/24VA(DC)		
Maximum voltage	drop/ conducting resistance	_	0.6V	2.2V	2.2V	0.06V (initial)		
IV.	1inimum <b>l</b> oad	_	_			2mA/DC power		
Le	akage current	_	< 0.1mA/30VDC			_		
Maximum output	ON→OFF	200nS	2μS	15µS		10mS		
delay time	0FF→0N	200113			lμS	101113		
Output	t status indication		Displayed by LED: Light when "ON", dark when "OFF"					
Over o	current protection		N/A					
Isolation type			Optical isolation, 500	Electromagnetic isolation 1500VAC, 1 minute				
SINK/SOURCE output type		Independent dual terminals for arbitrary connection	Choose SINK/SOURCE by models and non-exchangeable			Can be arbitrarily set to SINK/SOURCE output		

 $<sup>\</sup>hbox{\rm *:Half of the maximum frequency while A/B phase output}\\$ 

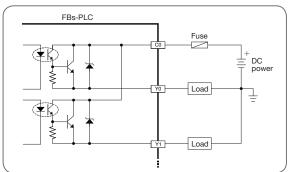
## $Wiring of 5VDC differential \ output \ (with \ frequency \ up \ to \ 920 KHz, for \ high \ speed \ or \ high \ noise \ environments)$



### Wiring of transistor single-end SINK output



### Wiring of transistor single-end SOURCE output



### Wiring of relay single-end output

